

JPW

**TRANSMITTAL LETTER
(General - Patent Pending)**

Docket No.
SEC.1148

In Reply, Please Refer To: **Jeong-Hwan YANG**

Application No. 10/801,651	Filing Date 03/17/2004	Examiner Walter Lindsay, Jr.	Customer No. 20987	Group Art Unit 2812	Confirmation No. 1694
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Title: **SEMICONDUCTOR DEVICE WITH DIFFERENT LATTICE PROPERTIES**

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

in the above identified application.

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Dated: **24 January 2005**

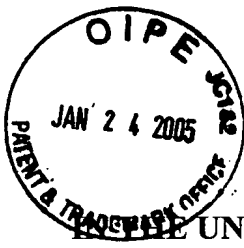
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____.

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Atty. Docket No.: SEC.1148

UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of :
Jeong-Hwan YANG :
Serial No.: 10/801,651 : Group Art Unit: 2812
Filed: March 17, 2004 : Examiner: Walter Lindsay, Jr.
For: SEMICONDUCTOR DEVICE WITH DIFFERENT LATTICE PROPERTIES

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

U.S. Patent and Trademark Office
Customer Window
401 Dulany Street
Alexandria, VA 22314

Sir:

In response to the Office Action of December 28, 2004, Applicants elect the invention of Group I, Claims 10-25, drawn to a semiconductor device. The election is without traverse.

Respectfully submitted,

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